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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Max M. Yeung et al.  
Serial No.: 09/975,293  
Title: CONSTRUCTION OF AN OPTIMIZED SEC-DED CODE AND LOGIC FOR SOFT ERRORS IN SEMICONDUCTOR MEMORIES  
Filed: October 11, 2001  
Attorney Docket No.: 01-322 / 1496.00144  
Examiner: Torres, J.  
Art Unit: 2133  
In Response To: Office Action mailed April 27, 2006

**CERTIFICATE OF MAILING**

I hereby certify that this letter, the response or amendment attached hereto are being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on May 10, 2006.

By: *Jan M. Dunbar*  
Jan M. Dunbar

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed April 27, 2006  
please amend the above-identified application as follows: